



Patent  
Attorney's Docket No. 015290-592

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of )  
Jeffrey HUNG et al. ) Group Art Unit: 1765  
Application No.: 10/075,602 ) Examiner: Unassigned  
Filed: February 15, 2002 )  
For: ETCHING PROCESS FOR ORGANIC )  
ANTI-REFLECTIVE COATING )

**RECEIVED**  
**APR 15 2002**  
**TC 1700**

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited was submitted in Application No. 09/002,007 upon which is based a claim for priority under 35 U.S.C. § 120.

U.S. Patent No. 4,910,122

U.S. Patent No. 5,126,289

U.S. Patent No. 5,256,564

U.S. Patent No. 5,269,879

U.S. Patent No. 5,308,742

U.S. Patent No. 5,316,980

U.S. Patent No. 5,437,961 *O<sub>2</sub> - CF<sub>4</sub> - Ar*

U.S. Patent No. 5,443,941

U.S. Patent No. 5,445,710 *Ar, O<sub>2</sub>, He (Ar or Kr)*

U.S. Patent No. 5,486,493

U.S. Patent No. 5,508,881

U.S. Patent No. 5,514,247

U.S. Patent No. 5,529,657  
U.S. Patent No. 5,554,560  
U.S. Patent No. 5,591,480  
U.S. Patent No. 5,591,677  
U.S. Patent No. 5,635,337  
U.S. Patent No. 5,639,389  
U.S. Patent No. 5,641,610  
U.S. Patent No. 5,655,110  
U.S. Patent No. 5,753,418  
U.S. Patent No. 5,883,007  
U.S. Patent No. 5,919,599  
U.S. Patent No. 5,980,768  
European Patent No. 0407169  
PCT Publication No. 097/07145

STANLEY WOLF et al., "Silicon Processing for the VLSI Era, Process Technology,  
Lattice Press, Sunset Beach, CA, Vol. 1, pp. 438-441

A. NISHIZAWA et al., "Dry Etching of Bottom Anti-Reflective-Coat and its Application  
to Gate Length Control", IEEE International Symposium on Semiconductor Manufacturing  
Conference Proceedings, XP-002099210, October 6-8, 1997, pages F13-F16

The documents are being submitted within 3 months of the filing or entry of the national  
stage of this application or before the first Office Action on the merits, whichever is later, therefore  
no fee or statement is required under 37 C.F.R. § 1.97(b).

Copies of the listed documents were previously submitted in prior application Serial No.  
09/002,007, filed December 31, 1997, upon which Applicants rely for the benefits provided in 35  
U.S.C. § 120. In accordance with 37 C.F.R. § 1.98(d), copies of the listed documents are not  
included.

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: Peter K. Skiff  
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Date: 4-12-02



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INFORMATION DISCLOSURE STATEMENT  
TRANSMITTAL LETTER

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

- ☒ No additional fee for submission of an IDS is required.
- ☐ The fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- ☐ A certification under 37 C.F.R. § 1.97(e), and the fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) are also enclosed.
- ☐ Charge \$\_\_\_\_\_ to Deposit Account No. 02-4800 for the fee due.
- ☐ A check in the amount of \$\_\_\_\_\_ is enclosed for the fee due.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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